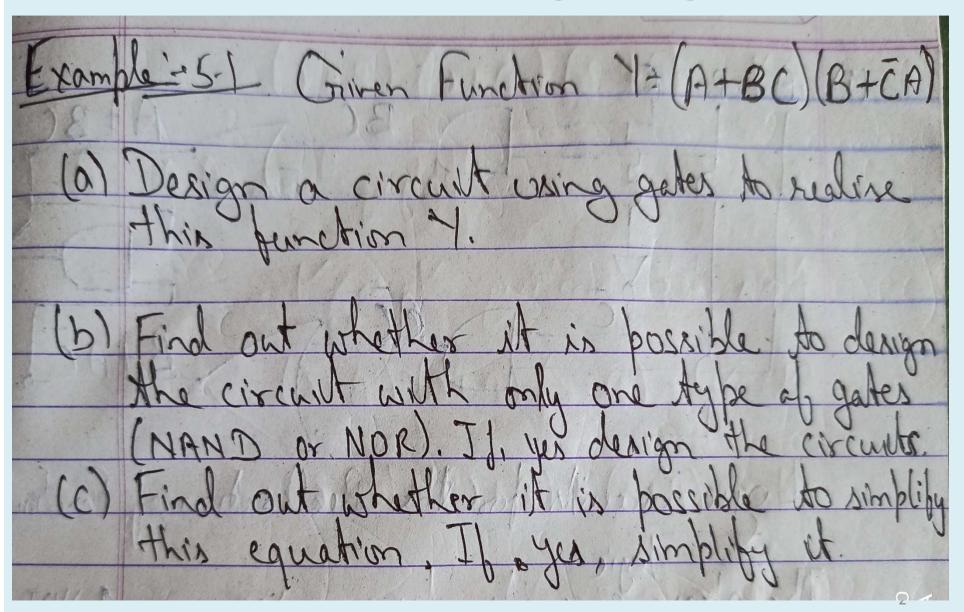
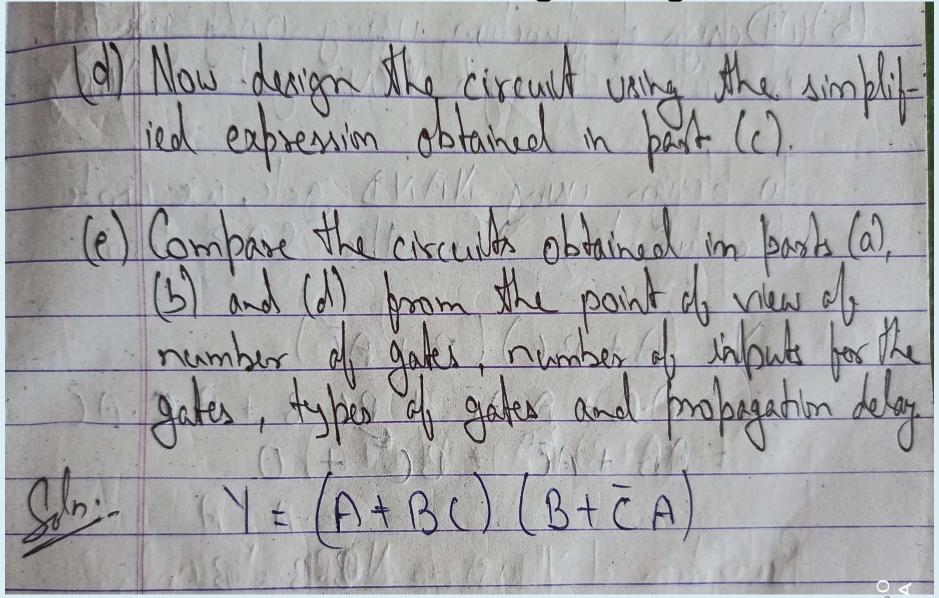
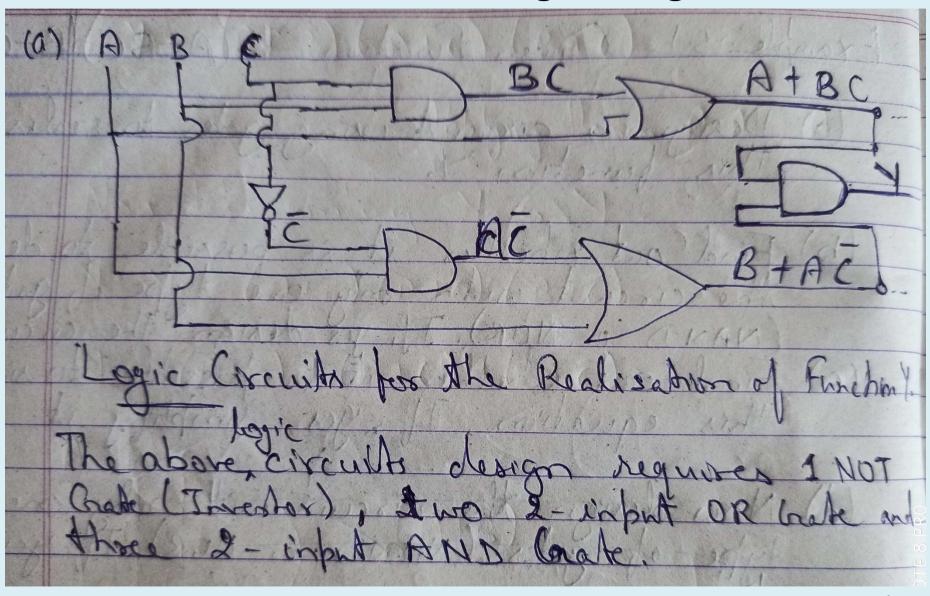
Paper 7, TDC Part-3 Chapter— 4, Combinational Logic Design Lecture - 2

By:

Mayank Mausam
Assistant Professor (Guest Faculty)
Department of Electronics
L.S. College, BRA Bihar University,
Muzaffarpur, Bihar

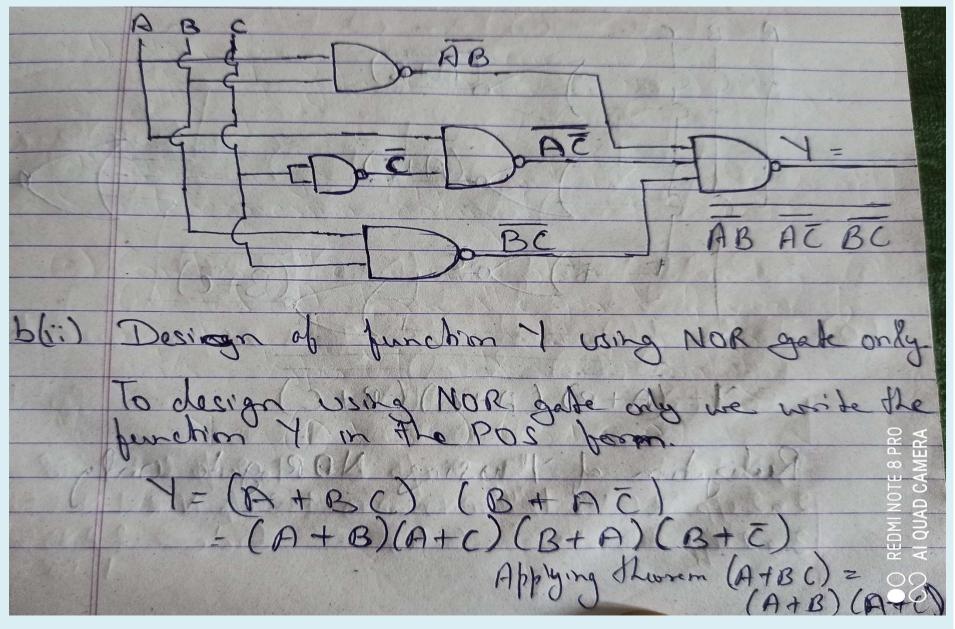


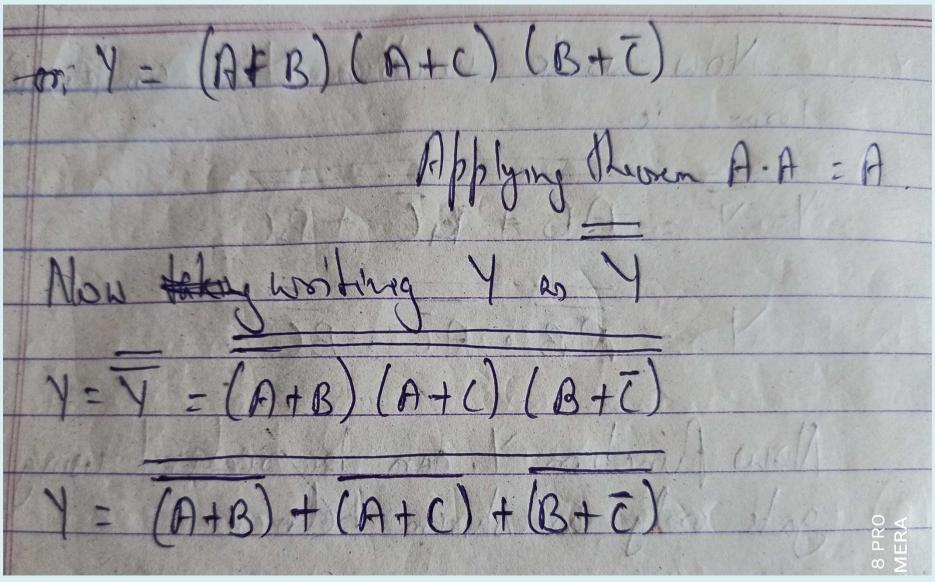


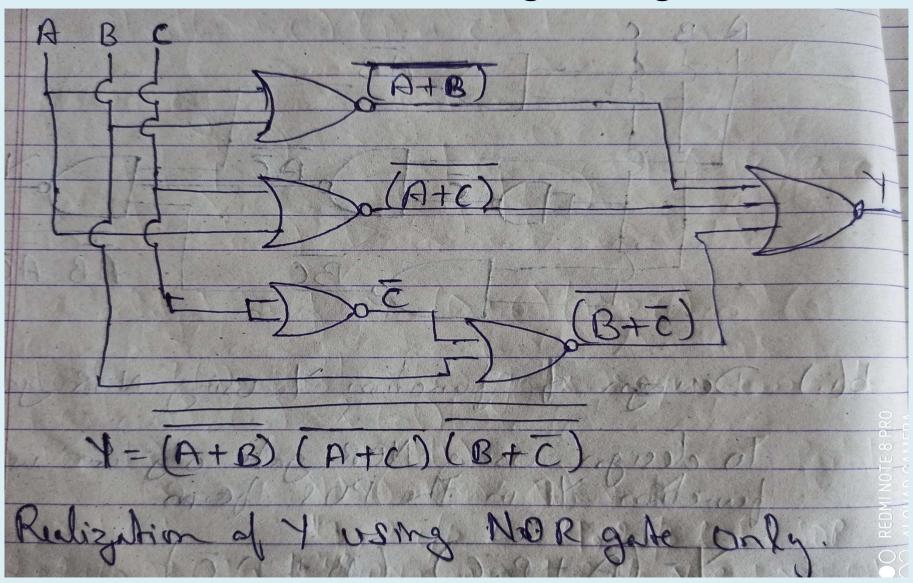


(5) (i) Design of Junction Using only gate that is either through NAND gate or NOR gal
is either through NAND gate or NOR got
is eliner (hrough NAND) gate or NOSE gat
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
the function of my the 20P form.
the function of in the 20P form.
CHUSTER THOCK DATE STORE VILLE TO THE MEDICAL TOWN OF THE PARTY OF THE PROPERTY OF THE PROPERT
Y= (A+BC)(B+AC)
= A.B + A.Ac + BC.B + BE.Ac
= A.B + A.T. + B.C. + B.C. A.C. = AB + AC + BC + O
(A) +(C) () & +(A, A LA)
B.B = B
C. E = 0) 2
Y = AB + AC + BC

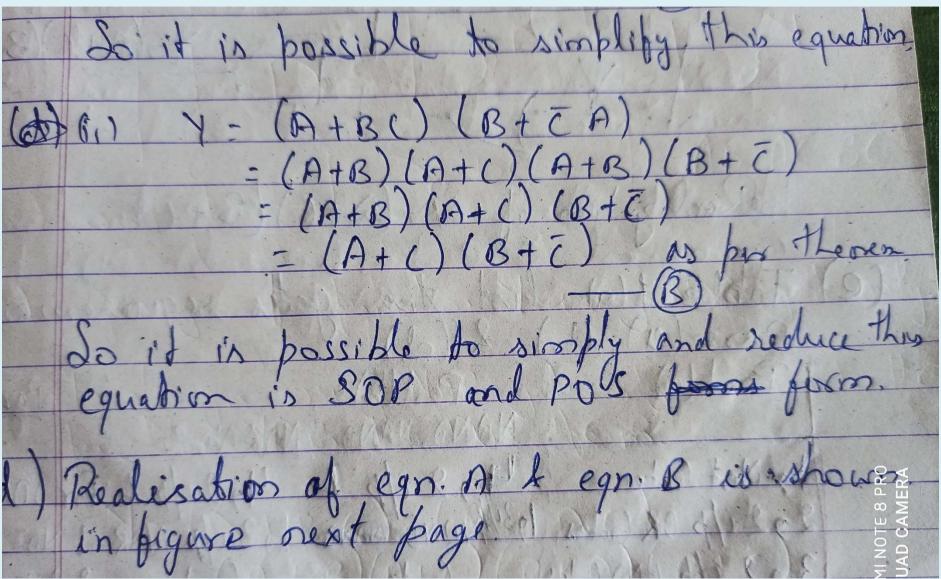
Now write function I in complemented
Farm 10.
1966-1112 - 1966
Y= Y = AB + BC + BC
Y = AB. AC. BC
(5) (5) (5) (8) (8) (8) (8) (8) (8) (8) (8) (8) (8
Now Auchin I can be realised using NAND
Now function I can be ocalised using NAND gate only as shown below.
PROFERA

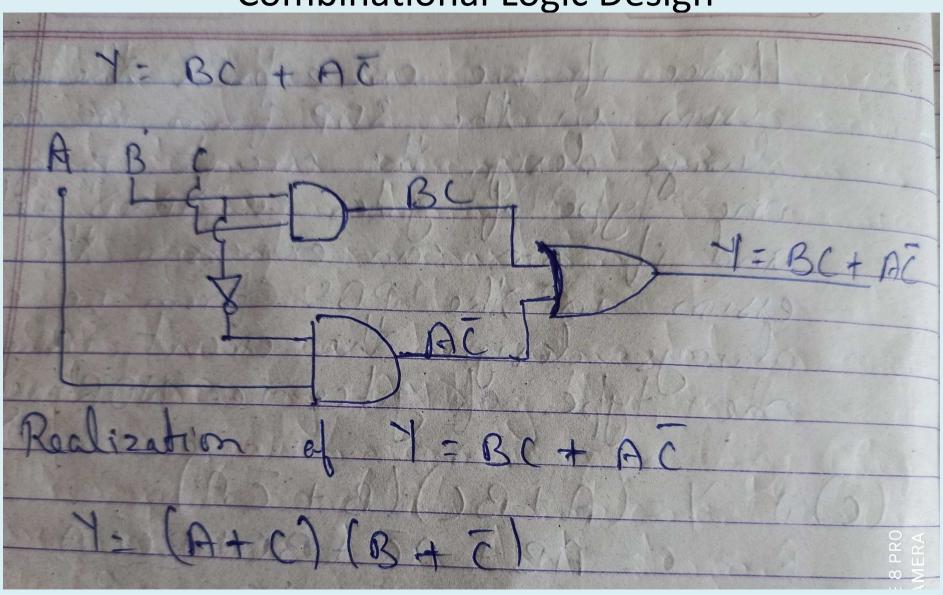


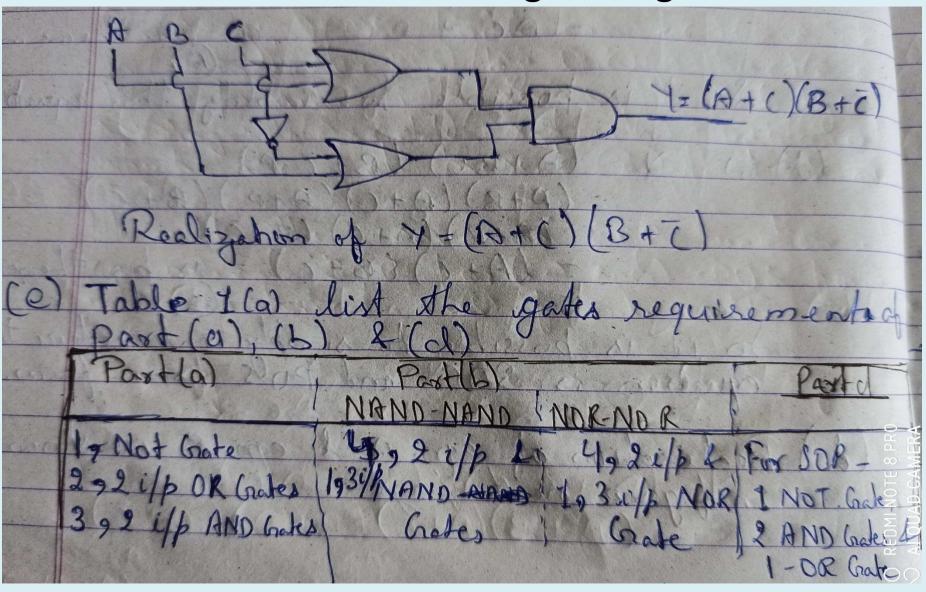


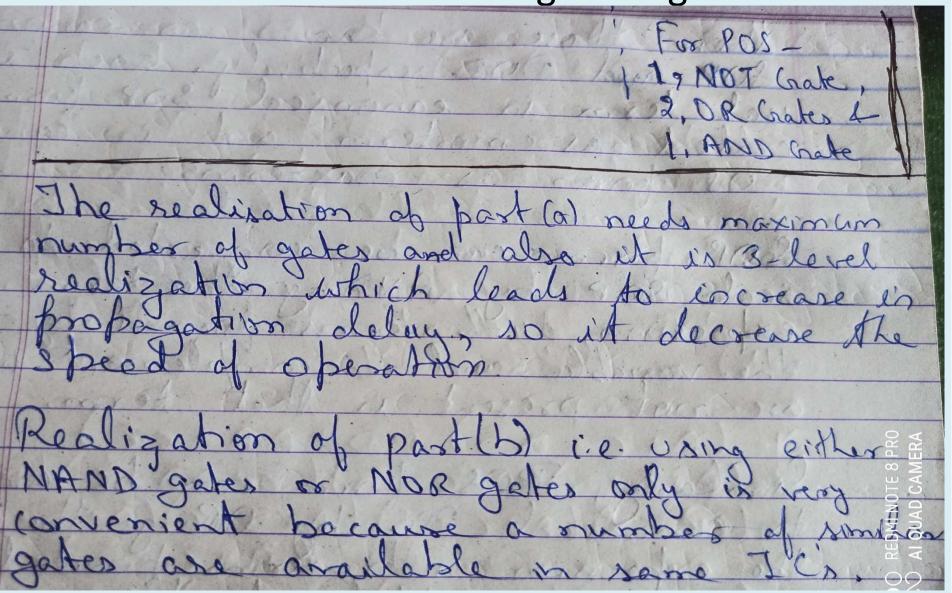


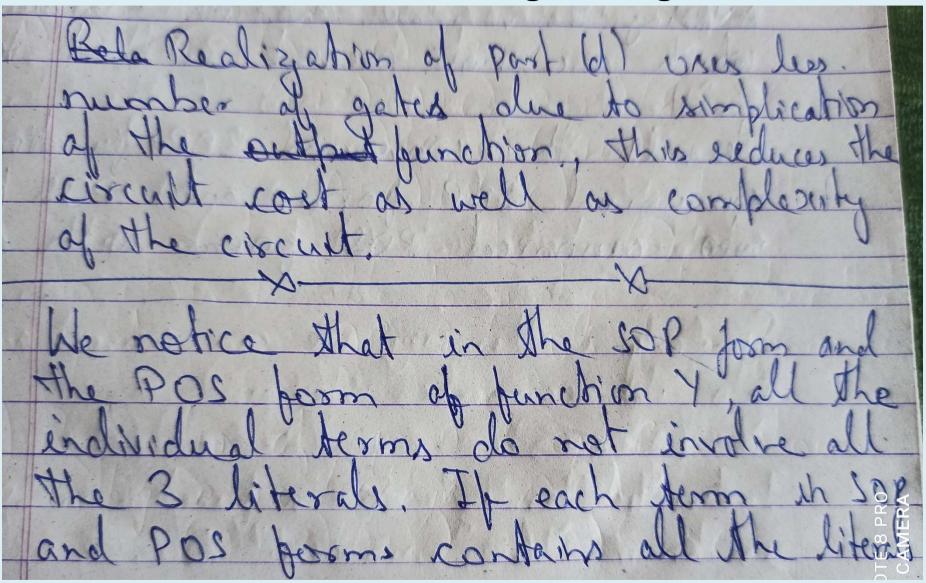
- Combinational Logic Design
Hence if we can express the expre-
ession in the SOP from we can
one type of gates i.e. "NAND" gate, and it we can expression the cape-
One type of gates in MAND gate,
and of the con expression the expr-
ession in the FUS from we can
altrays darigh the circuit wing only
ession in the POS from we can always design the circuit using only one type of gates i.e. "NOR" gate
C) (1) Y = (A+BC)(B+ ZA)
(M) / - (H 1 13C) (13 T C)
= AB + AC + BC + BCCA
= AB+AE+BC
- AC + BC as per through
A Section of the sect

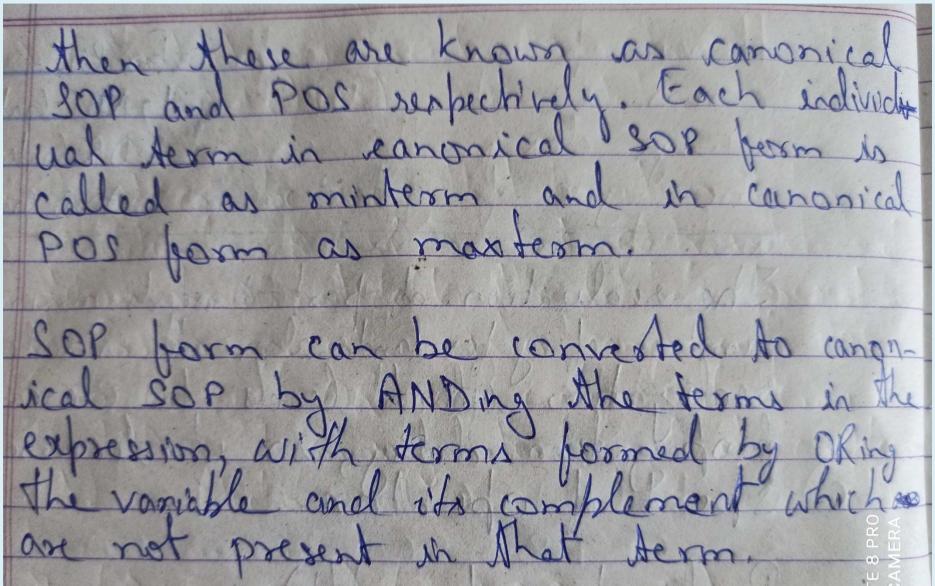


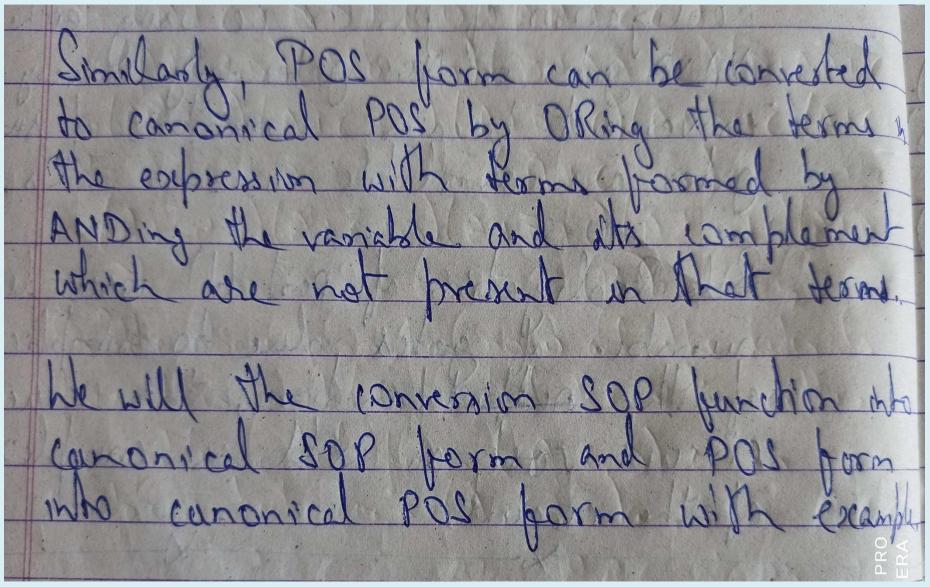












Number Systems and Codes

Refer book- Modern Digital Electronics by RP Jain.

Thank You