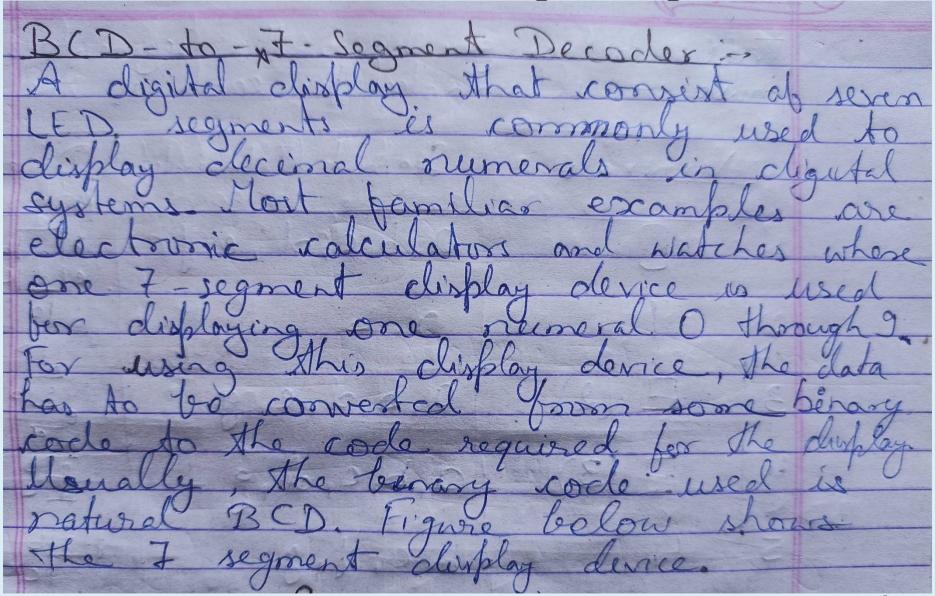
Paper 7, TDC Part-3 Chapter- 4, Combinational Logic Design Lecture - 16

By:

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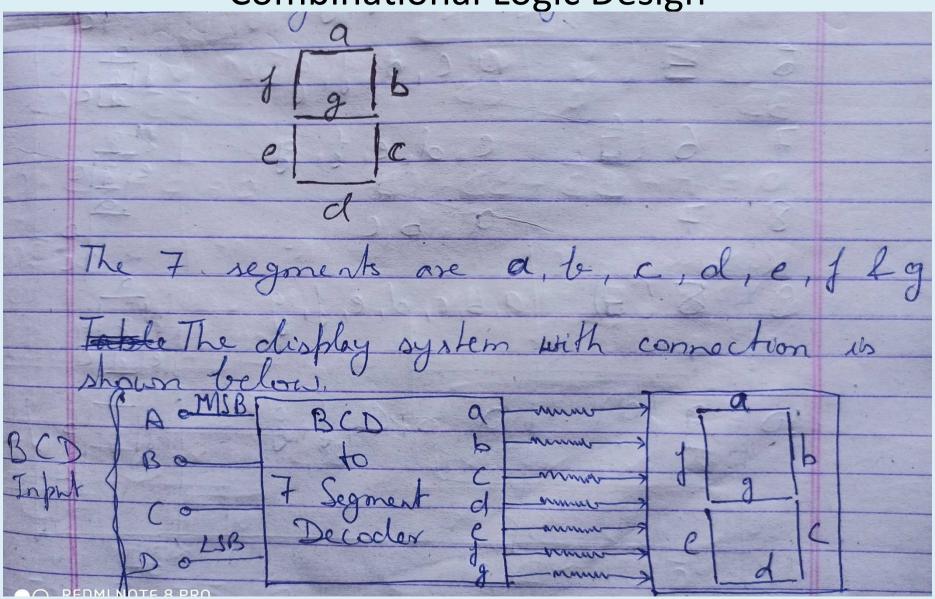
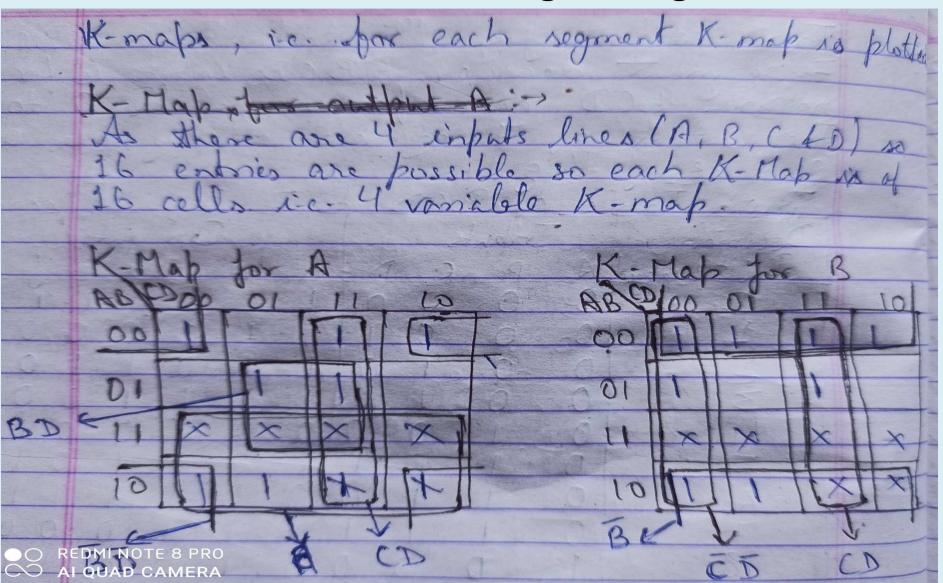


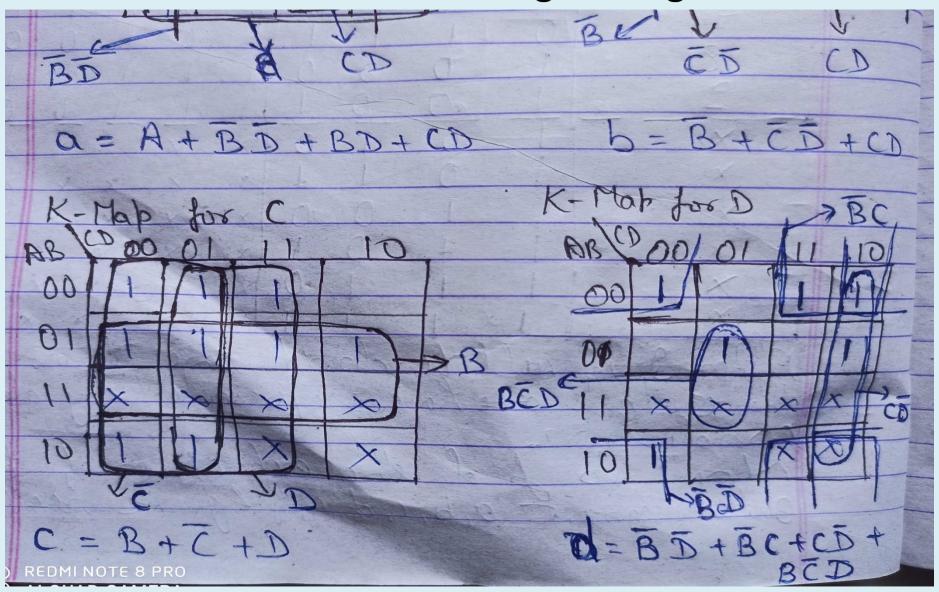
Table below greshours the segments which must be alluminated for each of the numer				
Numerals	Segments (Illuminated	Dinplay		
1 0	a, b, c, d, e, j	to the eldic		
2	On b	19		
3 2 (2)	[a,b,d,e,g]	et at		
4 = (3)	(a,b,c,d,g)	ale ale		
O REDMI NOTE 8 PRO	16,0,7,9	11215		

Combinational Ecolor Design				
	6	5 (/a,c,d, J, g	112 dlc
	7	6 (6)	c, d, e, f, g	\$ 9 e d C
1	8	7 7	9,b,c	10
	9	8 (B)	a, b, c, d, e, f, g	1 2 6
		6/0		elac
1	0	9(9)	0,6,6,1,9	1/2/5

Table below gives the struth stable of BCD to 7-segment decoder ABCD inputs is for the				
natural DID code for numerals 0 to 9.				
The outputs of I segments displays are a, b, C, cl, e, J. 4. g. BCD to F. Segment Decoder				
Decimal Inputs Outputs.				
0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0				
2 00101101				
4 0 1 0 0 0 1 1 0 0 1				
6 0 1 0 10 0 1 1 1 1 1				

Combinational Logic Design be don't





To be continued

Refer book- Modern Digital Electronics by RP Jain.

Thank You